

Appl. No.: 09/735,267
Amdt. dated December 9, 2003
Reply to Office action of September 11, 2003

Amendments to the Specification:

Please replace the Abstract with the following rewritten Abstract:

ABSTRACT

Q1
A computer system with a plurality of peripheral busses is adapted to permit multicast signals to be transmitted by a device on one peripheral bus ~~device~~ to multiple devices on the other peripheral bus ~~in a single bus cycle~~. In an exemplary embodiment, two PCI busses are provided, and master devices on either bus are capable of transmitting multicast signals to multiple targets on either bus. Targets of a multicast cycle are identified by a target identification signal on a first and a second multicast bus. A bus bridge relays the data for the multicast cycle between devices ~~bridges~~. In an exemplary embodiment, a sideband signal from the master to the bridge indicates a multicast signal has been transmitted on one of the PCI busses. In response, the bridge relays the multicast data to the second PCI ~~other~~ bus, while also transmitting a sideband signal to devices on the second bus indicating multicast data is being transmitted on that bus. Targets identified on the second ~~that~~ bus ~~then~~ ~~then~~ capture the multicast data.